

# Europass Curriculum Vitae

## Personal information

Surname(s) / First name(s)

Address(es)

Email(s)

Nationality(-ies)

Date of birth

Gender

**Durelli, Gianluca Carlo**

Via Martiri della Libertá, 21 - 24040 Misano di Gera d'Adda (BG) - Italy

durellinux@gmail.com

Italian

July, 24th 1987

Male

## Education and training

Date

Position

Institution

01/2013 - up to date

PhD Candidate in Computer Science and Engineering

Politecnico di Milano, Milano (Italy)

Date

Degree

Institution

Thesis

Grade

09/2009 - 04/2012

Master of Science in Computer Engineering

Politecnico di Milano, Milano (Italy)

*A<sup>2</sup>B* Application to bitstream: A semi-automated flow for reconfigurable MPSoC creation

110/110

Date

Role

Institution

08/2010 - 04/2011

Erasmus student - Master of Science in Computer Science

Uppsala Universitet, Uppsala (Sweden)

Date

Degree

Institution

Thesis

Grade

09/2006- 09/2009

Bachelor of Science in Computer Engineering

Politecnico di Milano, Milano (Italy)

Bebot: autonomous exploration and environmental analysis based on FPGA

106/110

Date

Degree

Institution

Grade

09/2001- 07/2006

Scientific High School diploma

Liceo Scientifico Galileo Galilei - indirizzo P.N.I., Caravaggio (Italy)

82/100

## Work experience

Date	12/2013 - 01/2014
Role	Teaching assistant Laboratory sessions for <i>Informatica B</i> , taught by Prof. Daniele Loiacono
Employer	Politecnico di Milano - Milano - Italy
Date	07/2013 - 11/2013
Role	Summer intern Summer internship in IBM High Performance Analytics group under the supervision of Fabrizio Petrini
Employer	IBM T.J. Watson Research Center - Yorktown Heights (NY)
Date	3/2013 - 6/2013
Role	Teaching assistant Laboratory sessions for <i>Informatica e Fondamenti di Informatica Medica</i> , taught by Prof. Marco D. Santambrogio
Employer	Politecnico di Milano - Milano - Italy
Date	10/2012 - 1/2013
Role	Teaching assistant Laboratory sessions for <i>Fondamenti di Informatica</i> , taught by Prof. Francesco Bruschi
Employer	Politecnico di Milano - Milano - Italy
Date	10/2012 - 1/2013
Role	Teaching assistant Laboratory sessions tutor for <i>Informatica B</i>
Employer	Politecnico di Milano - Milano - Italy
Date	10/2012 - 12/2012
Role	Web developer Development of a dynamic website to support learning of programming languages
Employer	METID - Politecnico di Milano - Milano - Italy
Date	03/2012 - 07/2012
Role	Teaching assistant Laboratory sessions tutor for <i>Prova Finale (Ingegneria del Software)</i> , taught by Prof. Carlo Ghezzi
Employer	Politecnico di Milano - Milano - Italy
Date	06/2010 - 10/2010
Role	Web developer Development of a dynamic website aimed at managing football tournaments ( <a href="http://sportland.milano.it">http://sportland.milano.it</a> )
Employer	Fubles SRL- Via Mascheroni, 14 - 20145 Milano - Italy

## Personal skills and competences

Mother tongue(s)

Other language(s)

*Self-assessment  
European level<sup>(\*)</sup>*

**English**

Language certifications

Computer skills and competences

## Italian

English

Understanding				Speaking				Writing	
Listening		Reading		Spoken interaction		Spoken production			
C1	Proficient user	C1	Proficient user	B2	Independent user	B2	Independent user	B2	Independent user

<sup>(\*)</sup> Common European Framework of Reference (CEF) level

Toefl IBT (2009): 97/120

Operating Systems: *Windows, Linux*

Programming Languages: *C, C++, Java, C#*

Scripting Languages: *Python, Bash (basic knowledge)*

Hardware Description Languages: *VHDL, Verilog (basic knowledge)*

SQL Database: *MySQL, Sqlite*

NoSQL Database: *Redis*

Web: *HTML, PHP, CSS, XML, JQuery, WebSocket*

## Publications

- RSP 2013 Riccardo Cattaneo, Christian Pilato, Gianluca C Durelli, Marco D Santambrogio, and Donatella Sciuto.  
Smash: A heuristic methodology for designing partially reconfigurable mpsoCs.  
In *Rapid System Prototyping (RSP), 2013 International Symposium on*, pages 102–108. IEEE, 2013
- DAC 2013 Davide B Bartolini, Riccardo Cattaneo, Gianluca C Durelli, Martina Maggio, Marco D Santambrogio, and Filippo Sironi.  
The autonomic operating system research project: achievements and future directions.  
In *Proceedings of the 50th Annual Design Automation Conference*, page 77. ACM, 2013
- FPL 2012 Christian Pilato, Andrea Cazzaniga, Gianluca Durelli, Andres Otero, Donatella Sciuto, and Marco D Santambrogio.  
On the automatic integration of hardware accelerators into fpga-based embedded systems.  
In *Field Programmable Logic and Applications (FPL), 2012 22nd International Conference on*, pages 607–610. IEEE, 2012
- FPL 2012 Alessandra Bonetto, Andrea Cazzaniga, Gianluca Durelli, Christian Pilato, Donatella Sciuto, and Marco D Santambrogio.  
An open-source design and validation platform for reconfigurable systems.  
In *Field Programmable Logic and Applications (FPL), 2012 22nd International Conference on*, pages 707–710. IEEE, 2012
- DSD 2012 Andrea Cazzaniga, Gianluca Durelli, Christian Pilato, Donatella Sciuto, and Marco D Santambrogio.  
On the development of a runtime reconfigurable multicore system-on-chip.  
In *Digital System Design (DSD), 2012 15th Euromicro Conference on*, pages 132–135. IEEE, 2012
- SAMOS 2012 Alessandra Bonetto, Andrea Cazzaniga, Gianluca C Durelli, Christian Pilato, Donatella Sciuto, and Marco D Santambrogio.  
Tabit: A framework for task graph to bitstream generation.  
In *Embedded Computer Systems (SAMOS), 2012 International Conference on*, pages 201–208. IEEE, 2012
- RECOSOC 2012 Gianluca Durelli, Christian Pilato, Andrea Cazzaniga, Donatella Sciuto, and Marco D Santambrogio.  
Automatic run-time manager generation for reconfigurable mpsoC architectures.  
In *Reconfigurable Communication-centric Systems-on-Chip (ReCoSoC), 2012 7th International Workshop on*, pages 1–8. IEEE, 2012
- RECOSOC 2012 MD Santambrogio, D Pnevmatikatos, K Papadimitriou, C Pilato, G Gaydadjiev, D Stroobandt, T Davidson, T Becker, T Todman, W Luk, et al.  
Smart technologies for effective reconfiguration: The faster approach.  
In *Reconfigurable Communication-centric Systems-on-Chip (ReCoSoC), 2012 7th International Workshop on*, pages 1–7. IEEE, 2012

## Additional Informations

### Personal interests

Sport (football, running), Books, Music